

TABLE 3: NUMBER OF POSSIBLE BUCK CONTROLLERS IN RELATION TO PROCESSOR PERFORMANCE AND PWM FREQUENCY

MIPS = 100				MIPS = 150			
PWM (kHz)	CPU (cycles)	CPU overhead	Loops	PWM (kHz)	CPU (cycles)	CPU overhead	Loops
125	800	5.1%	29.2	125	1200	3.4%	44.6
250	400	10.3%	13.8	250	600	6.8%	21.5
500	200	20.5%	6.1	500	300	13.7%	10.0
750	133	30.8%	3.6	750	200	20.5%	6.1
1000	100	41.0%	2.3	1000	150	27.3%	4.2